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	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

<p>WJ</p> <p>O.I.P.E.</p> <p>SCANNED <i>ADD</i> Q.A. <i>CH</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/505240	CONT/PRIOR D	CLASS 318	SUBCLASS U	ART UNIT 2837	EXAMINER
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APPLICANTS

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James Fitzgibbon
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Mark Siegler

TITLE

Method and apparatus for programming a logic board from switching power

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg. 10	Figs. Drwg. 12	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____				
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